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INFORMATION DISCLOSURE	FILING DATE: January 23, 2007	CONFIRMATION NO.: 9456	
STATEMENT BY APPLICANT	APPLICANT: Marcello Caramma		
•	CROUD ART INIT 2122	EVALUED.	
Sheet 1 of 1	GROUP ART UNIT: 2133	EXAMINER:	

U.S. PATENT DOCUMENTS

Examiner's Cite		U.S. Patent Doc	ument	Name of Patentee or Applicant of Cited	Date of Publication or Issue	
Initials * No. Number Kind Code		Document Document	of Cited Document MM-DD-YYYY			

FOREIGN PATENT DOCUMENTS

1 "	Cite No.	Foreign Patent Document			Name of Potentee or Applicant of Cited	Date of	Tuanalatian
		Office/ Country	Number	Kind Code	Document Cit	Publication of Cited Document MM-DD-YYYY	t (Y/N)
/SAC/		DE	19937506		Infineon Technologies AG	04-19-2001	N
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OTHER ART — NON PATENT LITERATURE DOCUMENTS

Examiner's Initials #	Cite No	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	Translation (Y/N)
/SAC/		PCT/GB2004/001770 - PCT International Preliminary Report w/written opinion, Nov 4, 2005	
/SAC/		Page, K. et al, "Improved architectures for the add-compare-select operation in long constraint length viterbi decoding", IEEE, Vol. 33, No. 1, Jan 1998, pp. 151-155.	

EXAMINER:	DATE CONSIDERED:
/Shelly A Chase /	06/30/2009

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